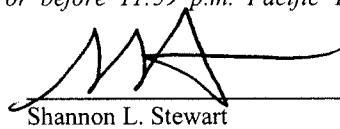


PATENT
RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2183

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

I hereby certify that this correspondence is being electronically filed with the United States Patent and Trademark Office on August 27, 2008 at or before 11:59 p.m. Pacific Time under the Rules of 37 CFR § 1.8.



Shannon L. Stewart

Appl No.	: 10/538,369	Confirmation No. 6028
Applicant	: Geoffrey F. Burns et al.	
Filed	: June 13, 2005	
Title	: MODULAR INTEGRATION OF AN ARRAY PROCESSOR WITHIN A SYSTEM ON CHIP	
TC/A.U.	: 2183	
Examiner	: Andrew Jacob Petranek	
Docket No.	: US-020543	
Customer No.	: 23363	

AMENDMENT AFTER FINAL ACTION

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Post Office Box 7068
Pasadena, CA 91109-7068
August 27, 2008

Commissioner:

In response to the Office action of July 31, 2008, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.